

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Isarnu Kobori et al.	Art Unit :	2811
Serial No. :	10/623,581	Examiner :	Stanetta Isaac
Filed :	July 22, 2003	Conf. No. :	6534
Title :	METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM TRANSISTOR		

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Applicants request consideration of the references listed on the attached PTO-1449 form.  
A copy of the listed U.S. patent can be provided upon request.

This statement is being filed before the receipt of a first Office Action on the merits after  
the filing of a RCE. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

11/12/07

  
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-024003	Application No. 10/623,581
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Isamu Kobori et al.	
		Filing Date July 22, 2003	Group Art Unit 2811

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	6,776,880	08/17/2004	YAMAZAKI			07/19/2000
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.